

(19)



Europäisches Patentamt
European Patent Office
Office européen des brevets



(11)

EP 0 855 796 A3

(12)

EUROPEAN PATENT APPLICATION

(88) Date of publication A3:
31.07.2002 Bulletin 2002/31

(51) Int Cl.7: H03H 17/02

(43) Date of publication A2:
29.07.1998 Bulletin 1998/31

(21) Application number: 98101319.6

(22) Date of filing: 26.01.1998

(84) Designated Contracting States:
AT BE CH DE DK ES FI FR GB GR IE IT LI LU MC
NL PT SE

Designated Extension States:
AL LT LV MK RO SI

(30) Priority: 27.01.1997 JP 2572697
14.03.1997 JP 7921397

(71) Applicant: Yozan Inc.
Tokyo 155 (JP)

(72) Inventors:

- Shou, Guoliang
Setagaya-ku, Tokyo 155 (JP)
- Motohashi, Kazunori
Setagaya-ku, Tokyo 155 (JP)

- Zhou, Changming
Setagaya-ku, Tokyo 155 (JP)
- Yamamoto, Makoto
Setagaya-ku, Tokyo 155 (JP)
- Zhou, Xuping
Setagaya-ku, Tokyo 155 (JP)
- Takatori, Sunao
Setagaya-ku, Tokyo 155 (JP)
- Qin, Xiaoling
Setagaya-ku, Tokyo 155 (JP)

(74) Representative: Grünecker, Kinkeldey,
Stockmair & Schwanhäusser Anwaltssozietät
Maximilianstrasse 58
80538 München (DE)

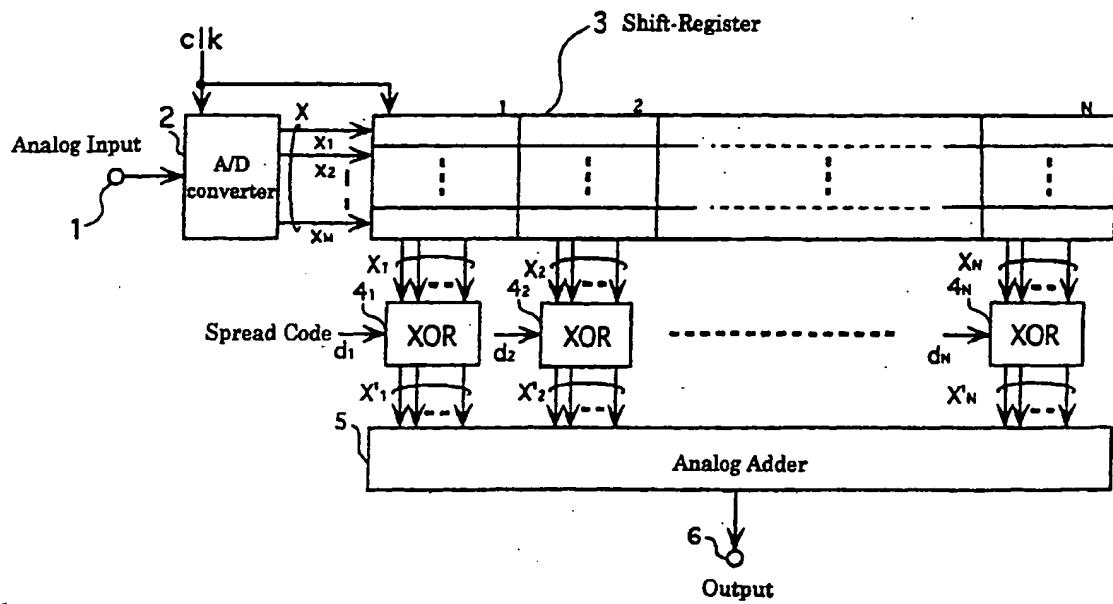
(54) Matched filter and filter circuit

(57) The invention provides according to a first aspect a low electric power consumption matched filter. The signal received at an input terminal is input to a shift-register having stages equal to the spread code length number after conversion into M-bit digital signals in an analog-to-digital converter. The outputs of the shift-register stages are input to EXCLUSIVE-OR circuits set corresponding to each stage, so that EXCLUSIVE-OR is performed between the outputs and corresponding spread code bits d_1 to d_N . The outputs of the EXCLUSIVE-OR circuits are analogously added in an analog adder and output from an output terminal. According to a second aspect the invention provides a filter circuit using an analog operation circuit to prevent lowering of operation accuracy caused by the residual charge. Input

analog signals successively undergo sampling and holding in each sampling and holding circuit, are multiplied by coefficients stored in a shift register by multiplication circuits, and added in addition circuit. Sample data transmission error storage is prevented by shifting coefficients in the shift register. Sampling and holding circuits and multiplication circuits are formed by analog operation circuits, and each include a switch for canceling the residual charge. The sampling and holding circuits and multiplication circuits normally working are refreshed sequentially by providing circuits for replacing their function. The addition circuit is set double and refreshed in the same way.

EP 0 855 796 A3

Fig. 1





DOCUMENTS CONSIDERED TO BE RELEVANT		Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.CI6)
Category	Citation of document with indication, where appropriate, of relevant passages		
A	US 4 559 606 A (LU NING H ET AL) 17 December 1985 (1985-12-17) * the whole document * ---	2	H03H17/02
A	US 5 239 496 A (VANCRAEYNEST JAN P) 24 August 1993 (1993-08-24) * column 5, line 64 - column 7, line 10 * ---	3	
X	GIDEON LEVITA: "THE PERFORMANCE OF DIGITAL MATCHED FILTERS FOR MULTILEVEL SIGNALS" IEEE TRANSACTIONS ON COMMUNICATIONS., vol. 31, no. 11, November 1983 (1983-11), XP002121767 IEEE INC. NEW YORK., US ISSN: 0090-6778 * page 1222, column 2, line 48 - page 1223, column 2, line 13; figure 4 *	1	
A	EP 0 609 095 A (SIGNAL PROCESSORS LTD) 3 August 1994 (1994-08-03) * column 13, line 32 - column 14, line 5; figure 8 * * column 15, line 8 - column 16, line 20 * * column 23, line 22 - column 24, line 50; figure 14 *	4	TECHNICAL FIELDS SEARCHED (Int.CI8) H03H
A	WO 92 09990 A (ITALTEL SPA) 11 June 1992 (1992-06-11) * the whole document *	4,5	
The supplementary search report has been based on the last set of claims valid and available at the start of the search.			
Place of search		Date of completion of the search	Examiner
THE HAGUE		11 June 2002	Coppieters, C
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			
T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons S : member of the same patent family, corresponding document			

**CLAIMS INCURRING FEES**

The present European patent application comprised at the time of filing more than ten claims.

Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims fees have been paid, namely claim(s):

No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims.

LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

see sheet B

All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.

As all searchable claims could be searched without effort justifying an additional fee, the Search Division did not invite payment of any additional fee.

Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims:

None of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims:



European Patent
Office

LACK OF UNITY OF INVENTION
SHEET B

Application Number
EP 98 10 1319

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

1. Claims: 1-3

Matched filter

1.1. Claims: 4-8

Filter circuit comprising refreshing means for
canceling offset voltage.

Please note that all inventions mentioned under item 1, although not necessarily linked by a common inventive concept, could be searched without effort justifying an additional fee.

ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.

EP 98 10 1319

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on. The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

11-06-2002

Patent document cited in search report		Publication date		Patent family member(s)	Publication date
US 4559606	A	17-12-1985		DE 3483985 D1 EP 0131260 A2 ES 534188 D0 ES 8507715 A1 JP 1704204 C JP 3062332 B JP 60051338 A	28-02-1991 16-01-1985 01-08-1985 16-12-1985 14-10-1992 25-09-1991 22-03-1985
US 5239496	A	24-08-1993		NONE	
EP 0609095	A	03-08-1994		EP 0609095 A1 US 5500874 A	03-08-1994 19-03-1996
WO 9209990	A	11-06-1992		IT 1243963 B DE 69103770 D1 DE 69103770 T2 WO 9209990 A1 EP 0560815 A1	28-06-1994 06-10-1994 16-03-1995 11-06-1992 22-09-1993